



Our Ref. No.: 82225P0189R

#21
LDS
8-28-0

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Michael Powell, et al.

Serial No.: 08/887,680

Filed: July 3, 1997

For: METHOD AND APPARATUS FOR
EXTENDING COMPUTER ARCHITECTURE
FROM 32 TO 64 BITS

Examiner: David Y. Eng

Art Unit: 2155

RECEIVED

AUG 27 1961

Assistant Commissioner for Patents
Washington, D.C. 20231

RESPONSE TO OFFICE ACTION

Technology Center 2100

In response to the Office Action dated May 21, 2001, Applicants respectfully enclose the required documents necessary before this reissue application can be allowed, as requested by the Examiner. Specifically, Applicants submit the original patent (in compliance with 37 CFR 1.178), written Consent of the Assignee (in compliance with 37 CFR 1.172), and a Statement Under 37 CFR 3.373(b).

A Notice of Allowance is earnestly solicited at the earliest possible date. If the Examiner believes that a telephone conference would be useful in moving the application forward to allowance, the Examiner is encouraged to contact the undersigned at (310) 207-3800. If necessary, the Commissioner is hereby authorized to charge payment or credit any overpayment to Deposit Account No. 02-2666 for any additional fees. A duplicate copy of the Fee Transmittal is enclosed.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Dated:


Raul D Mat

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CERTIFICATE OF MAILING:

I hereby certify that this correspondence is being deposited as First Class Mail with the United States Postal Service in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on August 16, 2001.


 Lillian E. Rodriguez